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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Applicant:**

Louis Lippincott et al.

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Art Unit: 2621

Serial No.: 10/750,075

§ Examiner: Chikaodili E. Anyikire

Filed: December 31, 2003

§ Docket: ITL.1703US  
§ P17498

For: Motion Estimation Sum of all Differences (SAD) Array Having Reduced Semiconductor Die Area Consumption

§ Assignee: Intel Corporation

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## **REPLY TO OFFICE ACTION**

Sir:

In response to the office action mailed June 11, 2007, please amend the above-referenced patent application as follows:

Date of Deposit: September 11, 2007  
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.